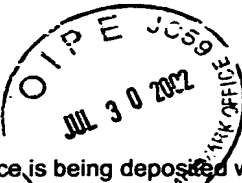


Docket No.: GR 00 P 1031



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GP/2183  
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By: [Signature]

Date: July 24, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Xiaoning Nie  
Applic. No. : 09/760,405  
Filed : January 12, 2001  
Title : Data-Processing Device  
Art Unit : 2183

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INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Dean M. Tullsen et al.: "Simultaneous Multithreading: Maximizing On-Chip Parallelism", Proc. Of 22<sup>nd</sup> Ann. Int. Symp. Comp. Archit., 1995, pp. 392-403;

Lance Hammond et al.: "A Single-Chip Multiprocessor", IEEE Computer, September 1997, pp. 79-85.

Respectfully submitted,

Mark P. Weichselbaum  
Reg. No. 43,248

[Signature]  
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